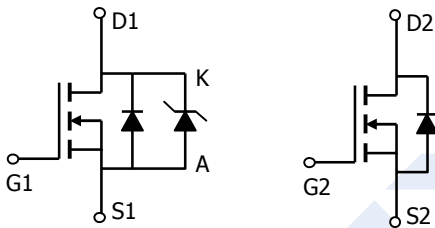
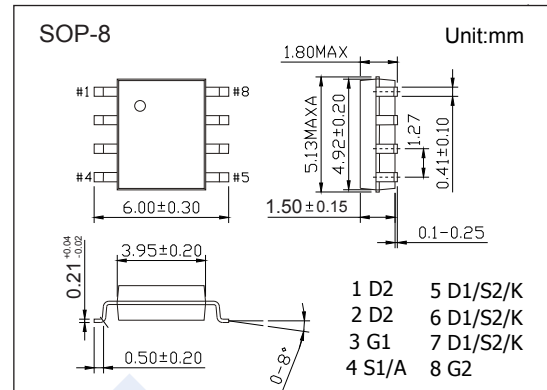


## Dual N-Channel MOSFET

### AO4906 (KO4906)

#### ■ Features

- $V_{DS} (V) = 30V$
- $I_D = 7 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 27m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 32m\Omega (V_{GS} = 4.5V)$
- $R_{DS(ON)} < 50m\Omega (V_{GS} = 2.5V)$
- $V_{DS} (V) = 30V, I_F = 3A, V_F < 0.5V @ 1A$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	MOSFET	Schottky	Unit
Drain-Source Voltage	$V_{DS}$	30		V
Gate-Source Voltage	$V_{GS}$	$\pm 12$		
Schottky Reverse Voltage	$V_{KA}$		30	
Continuous Drain Current	$I_D$	7		A
Pulsed Drain Current	$I_{DM}$	40		
Continuous Forward Current	$I_F$		3	A
Pulsed Diode Forward Current	$I_{FM}$		40	
Power Dissipation	$P_D$		2	W
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$		62.5	$^\circ C/W$
Thermal Resistance.Junction- to-Lead	$R_{thJL}$		40	
Junction Temperature	$T_J$		150	$^\circ C$
Storage Temperature Range	$T_{stg}$		-55 to 150	

## Dual N-Channel MOSFET

### AO4906 (KO4906)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V			1	μA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	0.7		1.4	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =7A			27	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =7A, T <sub>J</sub> =125°C			40	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A			32	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =5A			50	
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =5V	25			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =5A	12	16		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz		846	1050	pF
Output Capacitance	C <sub>oss</sub>			96		
Reverse Transfer Capacitance	C <sub>rss</sub>			67		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		1.24	3.6	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =15V, I <sub>D</sub> =7A		9.6	12	nC
Gate Source Charge	Q <sub>gs</sub>			1.6		
Gate Drain Charge	Q <sub>gd</sub>			3		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, R <sub>L</sub> =2.2Ω, R <sub>GEN</sub> =3Ω		3.2	4.8	ns
Turn-On Rise Time	t <sub>r</sub>			4.1	6.2	
Turn-Off DelayTime	t <sub>d(off)</sub>			26.3	40	
Turn-Off Fall Time	t <sub>f</sub>			3.7	5.5	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =5A, di/dt=100A/us		15.5	20	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			7.9	12	
Body-Diode + Schottky Continuous Current	I <sub>S</sub>				3	A
Diode + Schottky Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			1	V
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> =1A			0.5	
Maximum reverse leakage current	I <sub>rm</sub>	V <sub>R</sub> =30V			0.05	mA
		V <sub>R</sub> =30V, T <sub>J</sub> =125°C			10	
		V <sub>R</sub> =30V, T <sub>J</sub> =150°C			20	
Junction Capacitance	C <sub>T</sub>	V <sub>R</sub> =15V		37		pF

Note. The static characteristics in Figures 1 to 6 are obtained using 300 μs pulses, duty cycle 0.5% max.

#### ■ Marking

Marking	4906 KA****
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## Dual N-Channel MOSFET AO4906 (KO4906)

■ Typical Characteristics

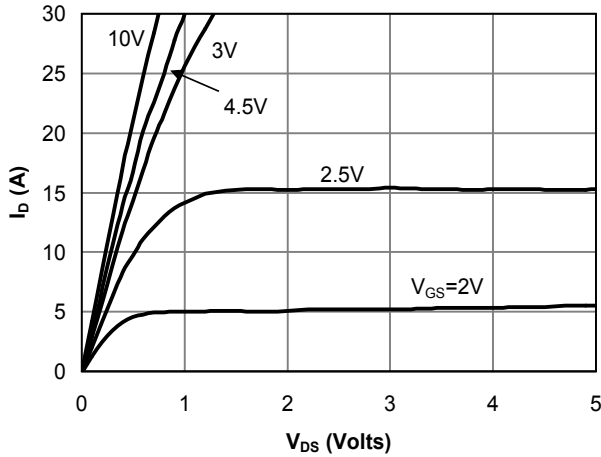


Fig 1: On-Region Characteristics

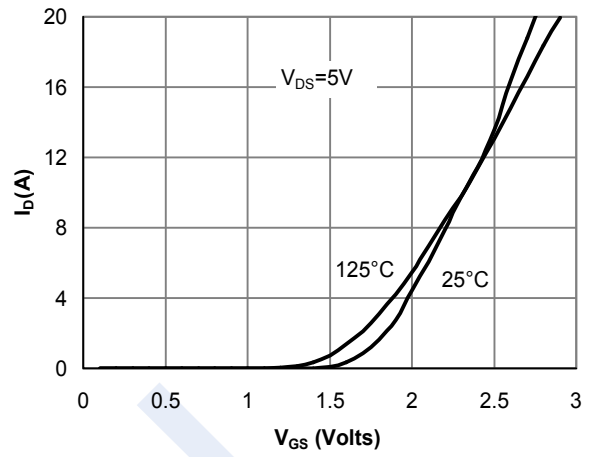


Figure 2: Transfer Characteristics

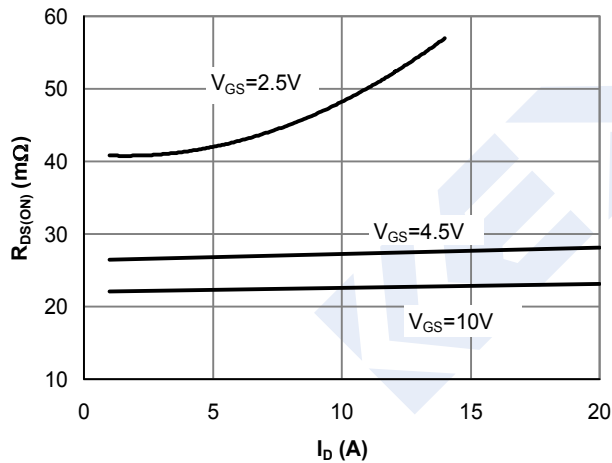


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

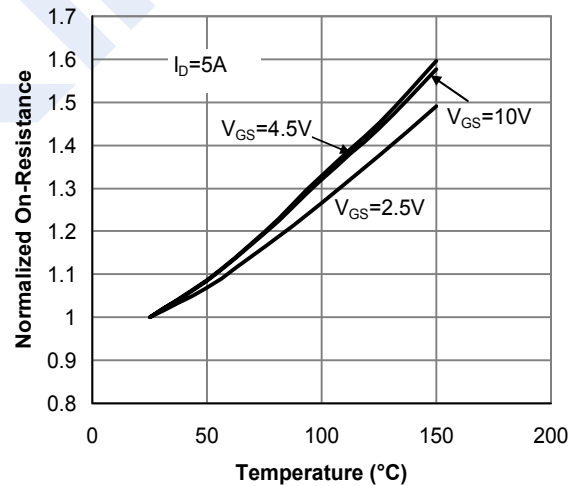


Figure 4: On resistance vs. Junction Temperature

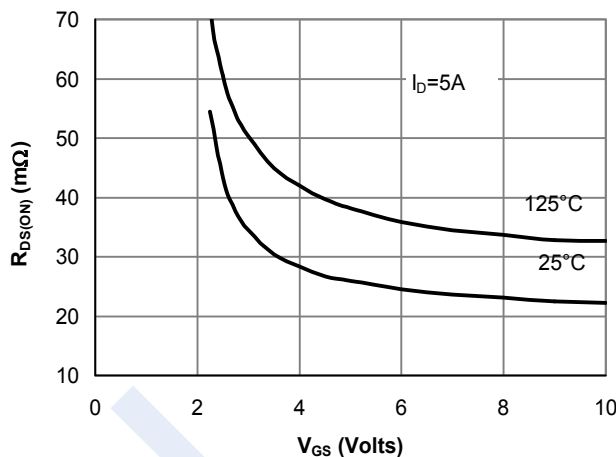


Figure 5: On resistance vs. Gate-Source Voltage

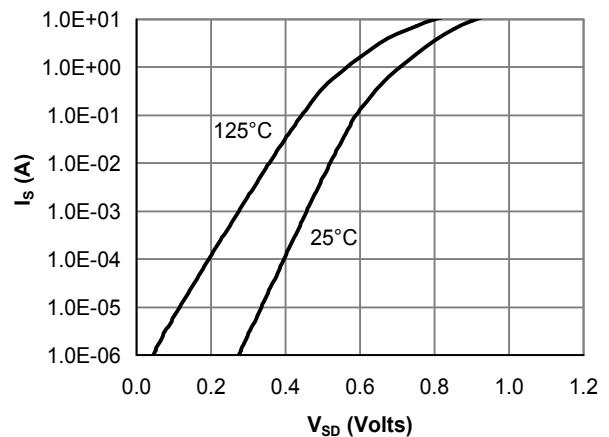


Figure 6: Body-Diode Characteristics

## Dual N-Channel MOSFET AO4906 (KO4906)

■ Typical Characteristics

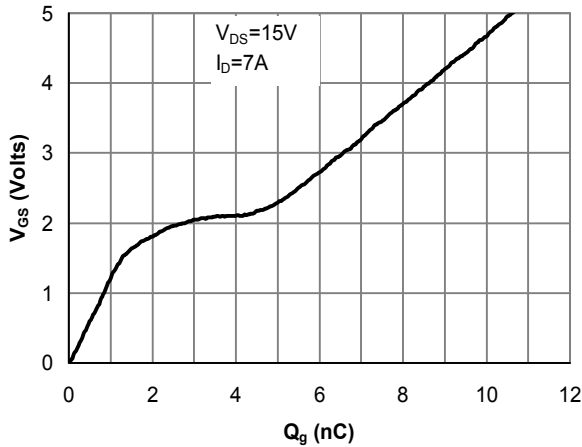


Figure 7: Gate-Charge Characteristics

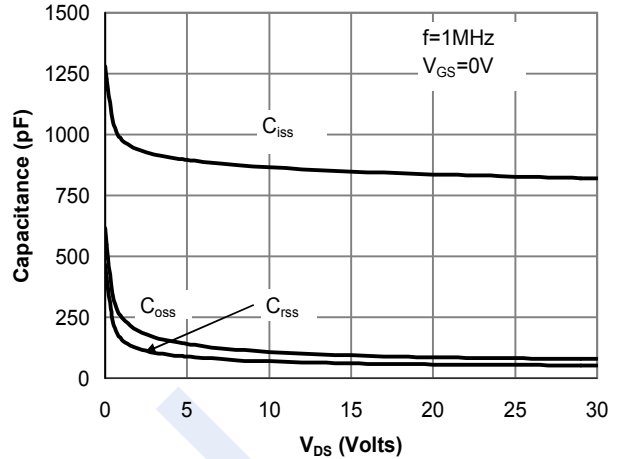


Figure 8: Capacitance Characteristics

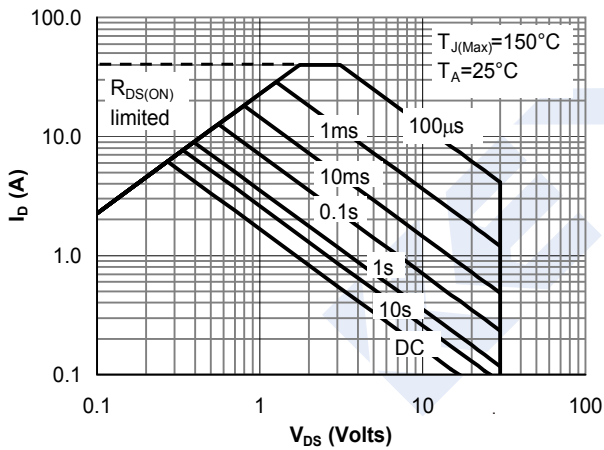


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

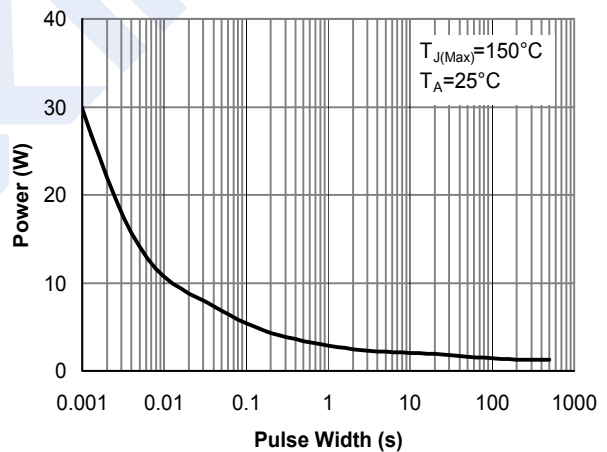


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

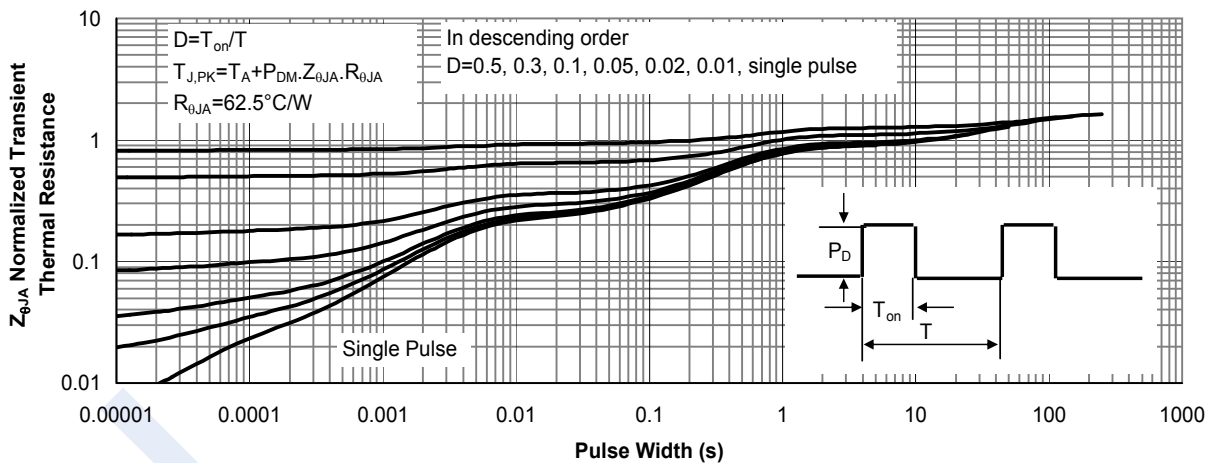


Figure 11: Normalized Maximum Transient Thermal Impedance

## Dual N-Channel MOSFET AO4906 (KO4906)

■ Typical Characteristics

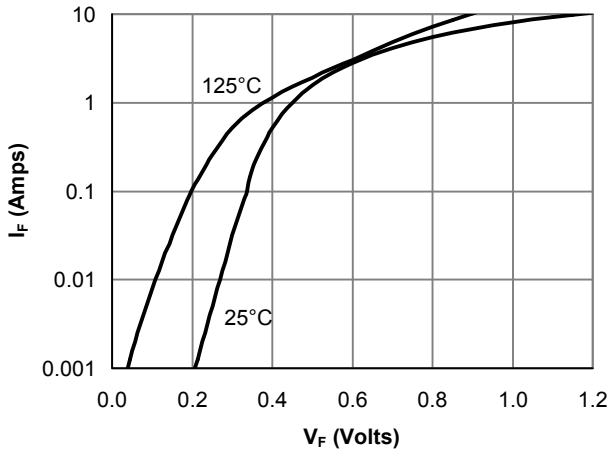


Figure 12: Schottky Forward Characteristics

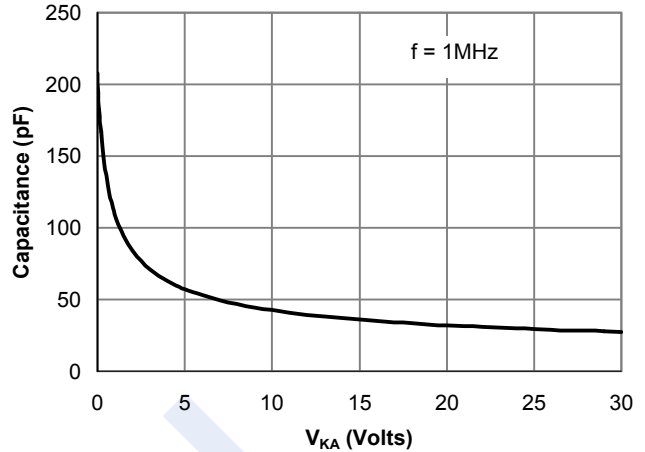


Figure 13: Schottky Capacitance Characteristics

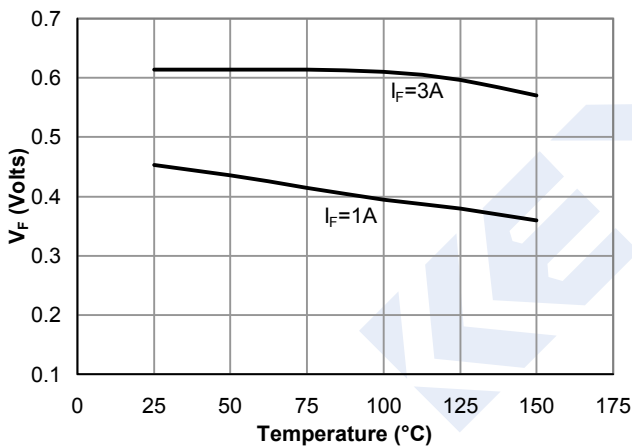


Figure 14: Schottky Forward Drop vs. Junction Temperature

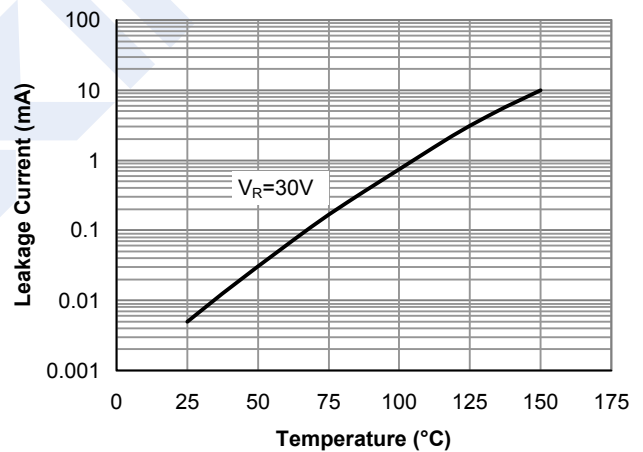


Figure 15: Schottky Leakage current vs. Junction Temperature

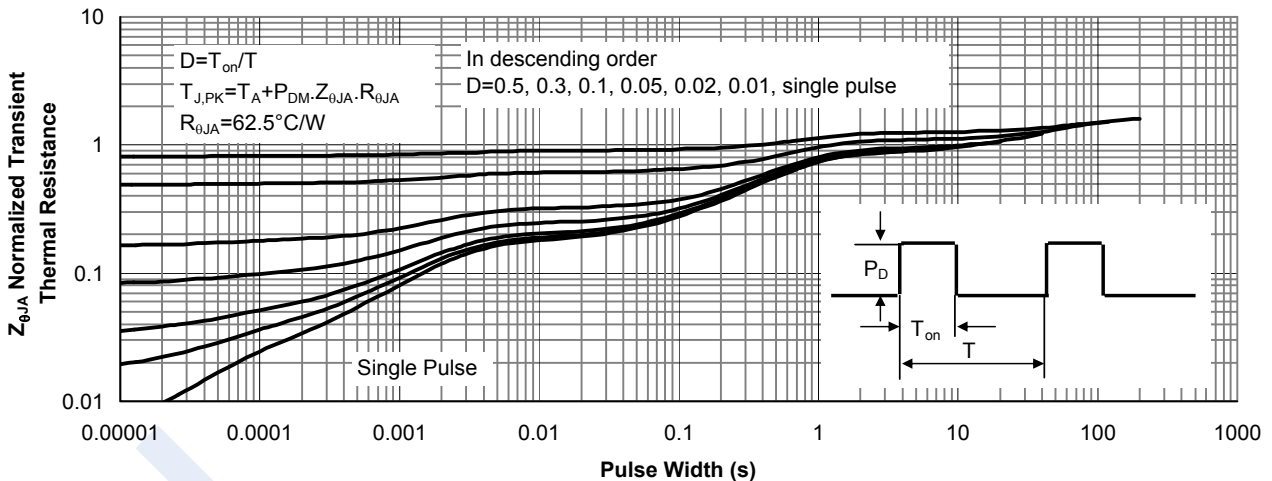


Figure 15: Schottky Normalized Maximum Transient Thermal Impedance